

AMENDMENTS TO THE SPECIFICATION

IN THE TITLE OF THE INVENTION:

Please replace the title of the application as follows:

--METHOD AND APPARATUS FOR ERROR CORRECTION USING A PLURALITY
OF ERROR CORRECTION CODES—

IN THE SPECIFICATION:

Please replace paragraph [0005] with the following amended paragraph:

[0005] Next, an operation will be described. At the error detection code coding circuit 34, a 16-bit error detection code (CRC) is added to data (38 in FIG. 20). At the fixed bit insertion circuit 35, a 5-bit "0" fixed data as a tail bit of a convolutional code is added to the data generated by the error detection code coding circuit 34, and the CRC check bit. After the check bit, (39 in FIG. ~~29~~ 20). At the convolutional coding circuit 36, convolutional coding of a rate 1/2 is carried out at a constraint length 6 (40 in FIG. 20). At the interleave generation circuit 37, a convolutional coded sequence is dispersed timewise (41 in FIG. 20) to increase resistance to a burst error.